# IEEE VPPC'2017 Conference December, 11th-14th 2017, Belfort, France



## MEGEVH Special Session on

### HARDWARE-IN-THE-LOOP TESTING FOR VEHICLE APPLICATIONS



Chair: Prof. Alain BOUSCAYROL, University of Lille1, L2EP, MEGEVH network (France)
Alain.Bouscayrol@univ-lille1.fr

Co-chair: Mr Dragan ZUBER, Typhoon HIL (Serbia)) zuber@typhoon-hil.com

## **Call for Papers**

New electrified vehicles are developed to face the climate challenge and the petroleum resources depletion. In order to test different subsystems within the vehicle the Hardware-In-the-Loop (HiL) simulation is more and more used. In the simulation loop, a part of the subsystem models is replaced d by a real subsystem. This one cans thus be tested with the right interactions of the other parts which are simulated in real time.

HiL testing is of growing interest in industry. But many challenges have yet to be faced for high-quality tests of components/subsystems before their integration in the vehicle.

#### Topics of interest include, but are not limited to:

- Signal HiL testing
- Power HiL testing,
- · Tools and methods for advanced HiL testing
- Component testing
- Subsystem testing
- Application to cars, bus, trucks, subways trains, etc,

#### Deadlines:

Submission of abstracts: Mar. 31, 2017
Notice of acceptance: June 15, 2017
Submission of full papers: Sep. 15, 2017

All special session digests must be prepared and submitted in the same way as those for the conference regular tracks (see <a href="http://www.vppc2017.org/">http://www.vppc2017.org/</a>), except that the corresponding special session should be identified during submission.